

In the claims:

1. (Currently Amended) Computer system including a circuit board comprising:

- a bus ~~system~~ for transmitting address, data and control signals with selectable bus configuration;
- a plurality of bus sockets on said circuit board for connecting an adapter card with said bus signals, whereby the specification of said adapter card determines the configuration of the ~~associated bus~~; and
- an indicator for said bus ~~indicating~~ displaying said configuration, wherein said indicator is integrated in ~~said one of said bus sockets~~.

2. (Currently Amended) Computer system according to claim 1, ~~wherein said bus system comprises~~ further comprising a plurality of busses having independent selectable configurations and each bus comprises at least one socket with an integrated indicator ~~indicating~~ displays said bus configuration.

3. (Currently Amended) Computer system according to claim 1, wherein said bus ~~system~~ provides a plurality of selectable bus speeds and said indicator further ~~indicates~~ displays said selected bus speed.

4. (Currently Amended) Computer system according to claim 1, wherein said bus ~~system~~ provides a plurality of selectable bus modes and said indicator further ~~indicates~~ displays said selected bus mode.

5. (Currently Amended) Computer system according to claim 1, wherein ~~each~~ said one of said bus sockets further comprises an integrated control unit driving said indicator.

6. (Currently Amended) Computer system according to claim 5, wherein said one of said bus sockets comprises a plurality of connectors for connecting said plurality of data, address and control signals and wherein said control unit is coupled with at least one of said connectors.

7. (Currently Amended) Computer system according to claim ~~1~~3, wherein said indicator comprises a light emitting diode for each of said plurality of clock speeds.
8. (Previously Presented) Computer system according to claim 4, wherein said indicator comprises a light emitting diode for each of said bus modes.
9. (Currently Amended) Computer system according to claim 43, wherein said bus provides a plurality of selectable bus modes and ~~wherein~~ said indicator comprises a liquid crystal display for displaying said clock speed and bus mode.
10. (Currently Amended) Computer system according to claim 43, wherein said bus provides a plurality of selectable bus modes and said indicator comprises an alpha-numeric display for displaying said clock speed and bus mode.
11. (Currently Amended) Computer system according to claim 1, wherein said one of said bus sockets further comprises a switch for enabling a hot-plug mode.
12. (Currently Amended) Computer system according to claim 11, wherein said one of said bus sockets further comprises a hot-plug indicator for ~~indicating~~ displaying said hot-plug mode.
13. (Currently Amended) Computer system according to claim 11, wherein said one of said bus sockets further comprises hot-plug error indicator means for ~~indicating~~ displaying an error which occurred during a hot-plug mode.
14. (Previously Presented) Computer system according to claim 1, wherein said bus is a PCI-bus.
15. (Previously Presented) Computer system according to claim 1, wherein said bus is a PCI-X-bus.

16. (Currently Amended) Computer system according to claim 1, wherein said one of said bus sockets comprises an enlarged area in which said indicator is integrated.

17. (Currently Amended) Computer system according to claim 16, wherein said one of said bus sockets comprises left and right side walls, whereby an upper portion of one of said side walls is extended outwards to receive said integrated indicator.

18. (Currently Amended) Computer system according to claim 12, wherein said one of said bus sockets comprises an enlarged area in which said hot-plug switch and said hot-plug indicator are integrated.

19. (Currently Amended) Computer system according to claim 18, wherein said one of said bus sockets comprises left and right side walls, whereby an upper portion of one of said side walls is at least partially extended outwards to receive said integrated hot-plug switch and indicator.

20. (Currently Amended) Method of indicating a bus speed within a computer system having a system bus with a plurality of sockets for receiving system adapter cards, whereby the specification of said adapter cards determines the bus configuration, and whereby ~~said~~ at least one socket of said plurality of sockets comprises an integrated indicator, the method comprising the steps of:

- inserting an adapter card in one of ~~said~~ the plurality of bus sockets;
- determining and setting the bus speed for all sockets associated to said bus; and
- ~~indicating~~ displaying said bus configuration for said bus by said indicator.

21. (Currently Amended) Method according to claim 20, further comprising the steps of:

- providing an indicator for each socket; and
- ~~indicating~~ displaying said bus configuration for each socket depending on the determined bus configuration.

22. (Currently Amended) Method according to claim 20, wherein said system bus comprises a plurality of busses, said method further comprising the steps of:

- providing indicator means for each socket of each bus; and  
- ~~indicating~~ displaying said bus configuration for each socket of each bus depending on the determined bus configuration.

23. (Previously Presented) Method according to claim 20, wherein said bus configuration includes a bus speed.

24. (Previously Presented) Method according to claim 20, wherein said bus configuration includes a bus mode.

25. (Previously Presented) Method according to claim 20, wherein said bus configuration determination is performed during initialization of an inserted adapter card.

26. (Currently Amended) Method according to claim 24, further comprising the step of:

- determining a bus mode; and
- ~~indicating~~ displaying said bus mode by said indicator.

27. (Currently Amended) Socket for a configurable computer bus-system comprising a slot with a plurality of slot contacts for connecting to which a plurality of address, data and control signals can be supplied from said configurable computer bus, wherein said slot is designed to receive an adapter card which can be plugged into said socket slot to couple said adapter card with said configurable computer bus, and wherein said socket comprises ~~ing~~ an indicator for ~~indicating~~ displaying a respective bus configuration.

28. (Currently Amended) Socket according to claim 27, further comprising a second indicator for ~~indicating~~ displaying a bus speed.

29. (Currently Amended) Socket according to claim 27, further comprising a third indicator for ~~indicating~~ displaying a bus mode.

30. (Previously Presented) Socket according to claim 27, further comprising a switch for enabling a hot-plug mode.

31. (Currently Amended) Socket according to claim 27, further comprising a hot-plug indicator ~~indicating~~ displaying whether a hot-plug mode is enabled.

32. (Currently Amended) Socket according to claim 27, further comprising a hot-plug error indicator ~~indicating~~ displaying whether an error during a hot-plug mode occurred.

33. (Previously Presented) Socket according to claim 27, wherein said socket comprises an enlarged area in which said indicator is integrated.

34. (Currently Amended) Socket according to claim 33, wherein said socket comprises left and right side walls, whereby an upper portion of one of said side walls is extended outwards to receive said integrated indicator.

35. (Currently Amended) Socket according to claim 31, further comprising a switch for enabling a hot-plug mode, wherein said socket comprises an enlarged area in which said hot-plug switch and said hot-plug indicator are integrated.

36. (Currently Amended) Socket according to claim 35, wherein said socket comprises left and right side walls, whereby an upper portion of one of said side walls is at least partially extended outwards to receive said integrated hot-plug switch and indicator.

37. (Currently Amended) Socket according to claim 27, wherein said socket is a PCI socket.

38. (Previously Presented) Socket according to claim 27, wherein said socket is a PCI-X socket.

39. (Previously Presented) Socket according to claim 27, further comprising an integrated control unit for driving said indicator.

40. (Previously Presented) Socket according to claim 39, wherein said socket comprises a plurality of connectors for connecting said plurality of data, address and control signals and wherein said control unit is coupled with at least one of said connectors.